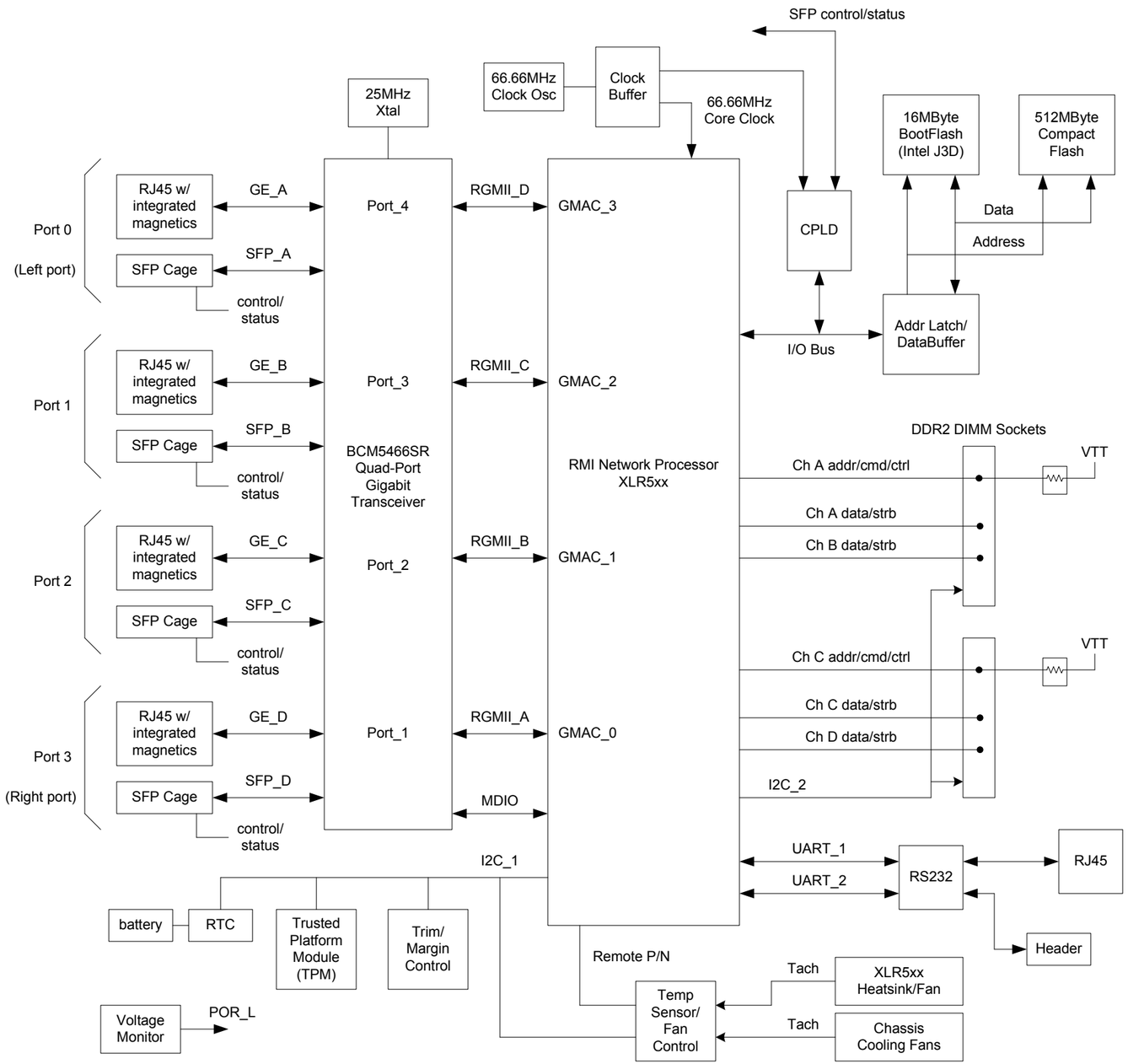
Dolcetto Functional Diagram

1/31/07



Configuration A:
XLR508 - 800MHz, 1.0V
512MByte DDR2-667 Memory
- 1x 512MByte DIMM, Ch A/B

Configuration B:
XLR516 - 800MHz, 1.0V
1GByte DDR2-667 Memory
- 1x 512MByte DIMM, Ch A/B
- 1x 512MByte DIMM, Ch C/D

Configuration C:
XLR532 - 800MHz, 1.0V
2GByte DDR2-667 Memory
- 1x 1GByte DIMM, Ch A/B
- 1x 1GByte DIMM, Ch C/D